Cache Memory

address a, 2 ports

1) direct mapped cache \((s=1)\)

2) set associative \((s\geq 1, s \geq 2)\)

2-way set associative

AMP Options

- 64 KB L1 cache 4-way associative
- 8 KB L1 cache 2-way associative
- 1 MB L2 cache 16-way

Total size is \(s \times E \times B\) bytes

if \(s=2\), called

per block