Review

Chap 1  Intro
  basic machine architecture
  fetch-execute cycle

Chap 2  Information Representation
  Boolean rep.
  text rep.  ASCII
number rep.
base 2, 16
negative
fixed points (Q notation)
floating-point (IEEE Std. 754)

Not responsible for:
§ 2.1.7 formal alg. and rings
translation func. § 2.2 82Tw(l)
§ 2.3 2-D figs
Chap 3  Machine-Level Programs

Instruction set Arch (ISA)

x86 ISA 132-bit version)

addressing modes
machine lang inst (assembly lang.)
pointers
arrays
heterogeneous data structures
call return seq.
stack frame

C lang limit

Not req. for: §3.14 x86 FP §3.15 inline assembly lang.
Exam:

- open book / open notes
- no computers
- no need for instructor manual
- questions take 2 forms:
  1. set of short answer questions
  2. set of longer in depth problems