

Review

Note Title

12/6/2011

Comprehensive

Weighted to material past Ch 3

Ch 1 - 3

Ch 5 through 5.6
code transformations

no microarchitecture

ch 6 (light on 6.1)

locality
memory hierarchy
cache org. & operation
pert. impl.

ch 7

linking process
" rules

symbol tables
ELF format

Chap 8 through 8.2

exceptions
processes

Chap 9 (2nd ed), 10 (1st ed)

memory management
virtual mem